

# Nixie Clock Main Board

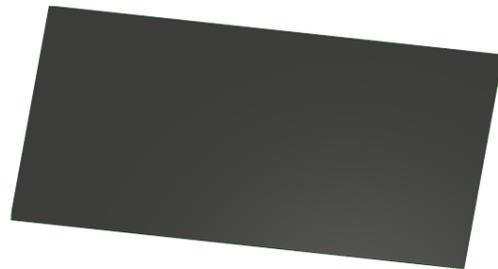
2026-02-17

Variant: DRAFT

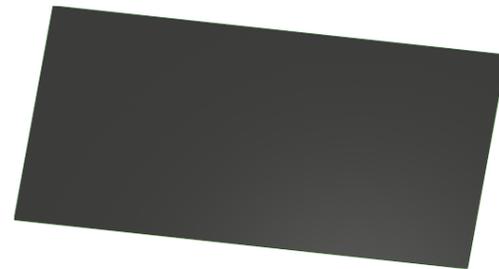
Rev + (Unreleased)

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## TOP VIEW



## BOTTOM VIEW



## NOTES

Comment

Not fitted components are marked as **X**

**DRAFT** - Very early stage of schematic, ignore details.

**PRELIMINARY** - Close to final schematic.

**CHECKED** - There shouldn't be any mistakes. Contact the engineer if you find any.

**RELEASED** - A board with this schematic has been sent to production.

Date: 17-Feb-2026

## DESIGN CONSIDERATIONS

**DESIGN NOTE:**  
Example text for informational design notes.

**DESIGN NOTE:**  
Example text for debug notes.

**DESIGN NOTE:**  
Example text for cautionary design notes.

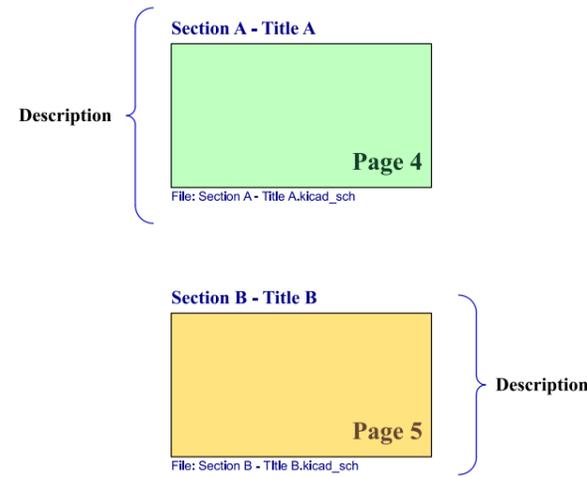
**DESIGN NOTE:**  
Example text for critical design notes.

**LAYOUT NOTE:**  
Example text for critical layout guidelines.

Comments:	Company:	Variant:	Git Hash:	
	Company Name	DRAFT	b199c49	
	Board Name:	Project Name:		
Sheet Title:	File Name:	Designer:	Date:	Revision:
/	Nixie_Tube_Clock.kicad_sch	Aidan Brzezinski	2025-01-12	+(Unreleased)
Sheet Path:	Reviewer:	Size:	Sheet:	
/		A3	1 of 7	

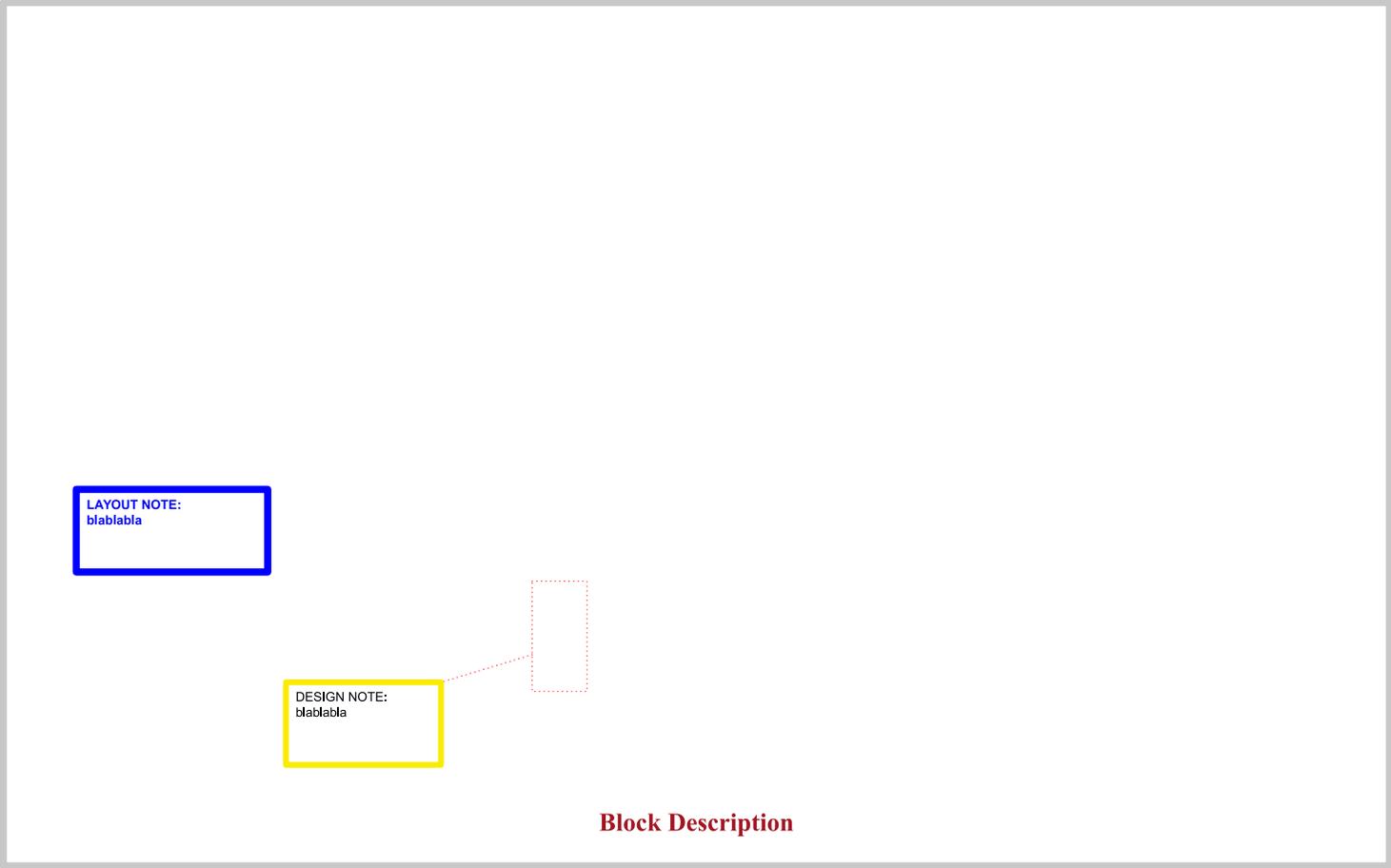


# [3] Project Architecture



	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: b199c49
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12
	Sheet Path: /Project Architecture/	Reviewer:	Size: A3	Revision: + (Unreleased)
		Board Name: <b>Nixie Clock Main Board</b>	Project Name: <b>Nixie Tube Clock</b>	
			Date: 2025-01-12	Revision: + (Unreleased)
			Size: <b>A3</b>	Sheet: <b>3</b> of <b>7</b>

# [4] Sheet Title A



	<b>Comments:</b> AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	<b>Company:</b> <b>Company Name</b>	<b>Variant:</b> DRAFT	<b>Git Hash:</b> b199e49	
		<b>Board Name:</b> <b>Nixie Clock Main Board</b>	<b>Project Name:</b> <b>Nixie Tube Clock</b>		
	<b>Sheet Title:</b> Sheet Title A	<b>File Name:</b> Section A - Title A.kicad_sch	<b>Designer:</b> Aidan Brzezinski	<b>Date:</b> 2025-01-12	<b>Revision:</b> + (Unreleased)
	<b>Sheet Path:</b> /Project Architecture/Section A - Title A/	<b>Reviewer:</b>	<b>Size:</b> <b>A4</b>	<b>Sheet:</b> <b>4</b> of <b>7</b>	

# [5] Sheet Title B

Block Description

DESIGN NOTE:  
blabla

DESIGN NOTE:  
blabla

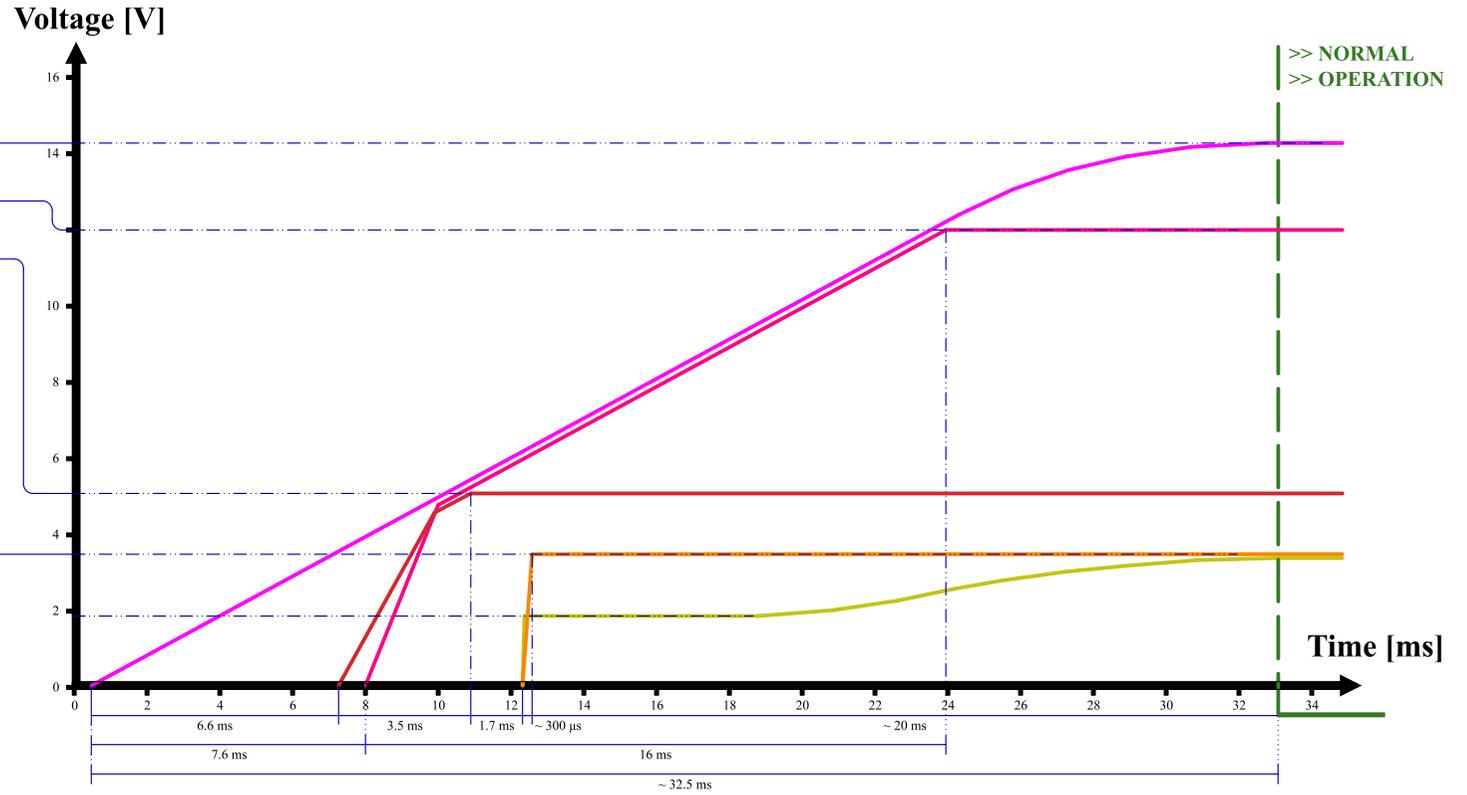
LAYOUT NOTE:  
blabla

Block Description

Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: Company Name	Variant: DRAFT	Git Hash: b199c49	
	Board Name: <b>Nixie Clock Main Board</b>		Project Name: <b>Nixie Tube Clock</b>	
Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
Sheet Path: /Project Architecture/Section B - Title B/		Reviewer:	Size: <b>A3</b>	Sheet: <b>5</b> of <b>7</b>

# [6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company:	Variant:	Git Hash:
		Company Name	DRAFT	b199e49
	Board Name:	Project Name:		
	<b>Nixie Clock Main Board</b>	<b>Nixie Tube Clock</b>		
Sheet Title:	File Name:	Designer:	Date:	Revision:
Power - Sequencing	Power - Sequencing.kicad_sch	Aidan Brzezinski	2025-01-12	+ (Unreleased)
Sheet Path:	Reviewer:	Size:	Sheet:	
/Power - Sequencing/		<b>A4</b>	<b>6</b> of <b>7</b>	

# [7] Revision History

	Comments:	Company:	Variant:	Git Hash:
		Company Name	DRAFT	b199e49
		Board Name:	Project Name:	
		<b>Nixie Clock Main Board</b>	<b>Nixie Tube Clock</b>	
Sheet Title:	File Name:	Designer:	Date:	Revision:
Revision History	Revision History.kicad_sch	Aidan Brzezinski	2025-01-12	+ (Unreleased)
Sheet Path:	Reviewer:	Size:	Sheet:	
/Revision History/		<b>A4</b>	<b>7 of 7</b>	