

Nixie Clock Main Board

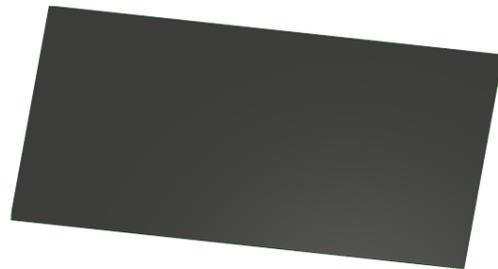
2026-02-17

Variant: DRAFT

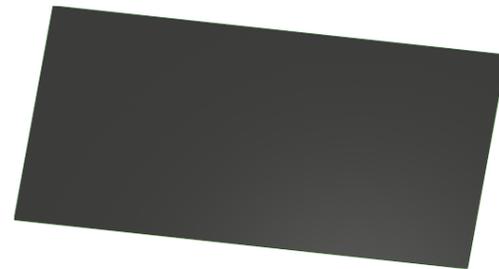
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as **X**

DRAFT - Very early stage of schematic, ignore details.

PRELIMINARY - Close to final schematic.

CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.

RELEASED - A board with this schematic has been sent to production.

Date: 17-Feb-2026

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

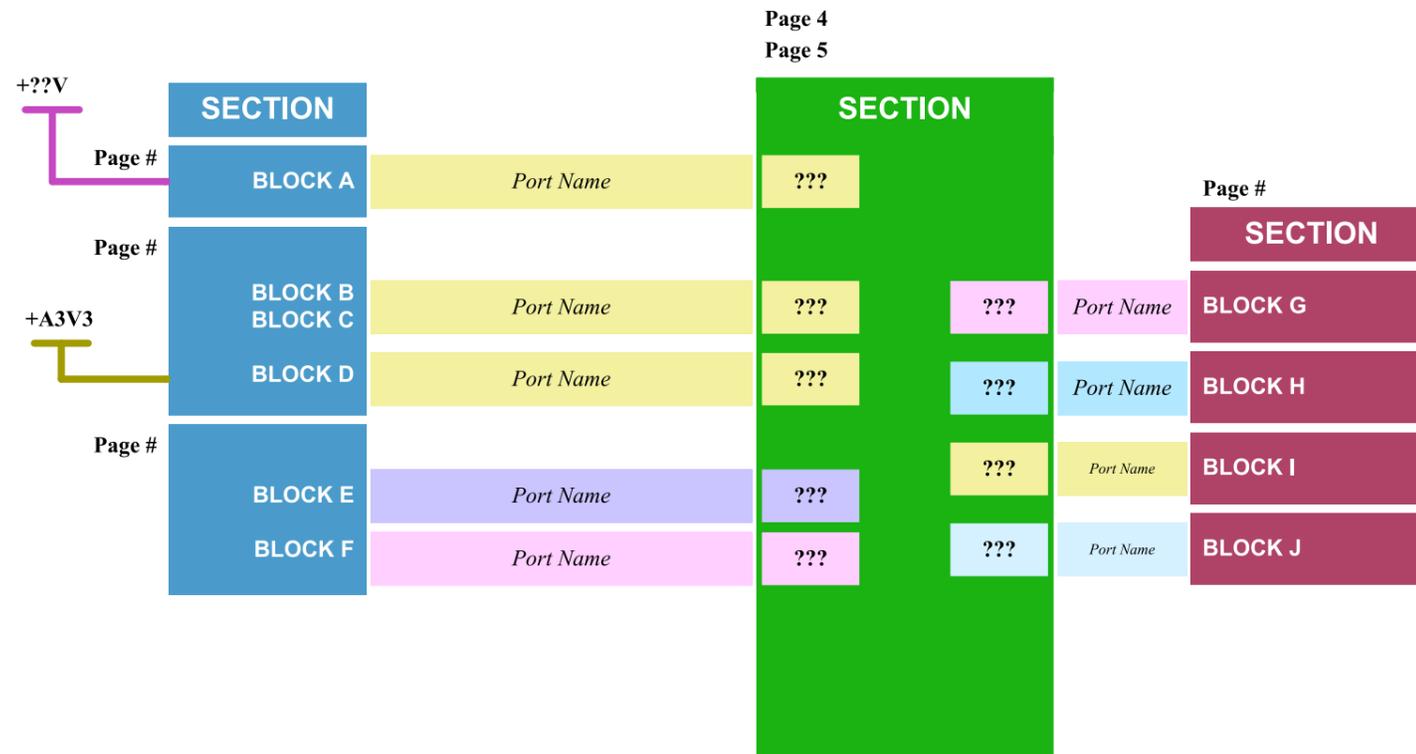
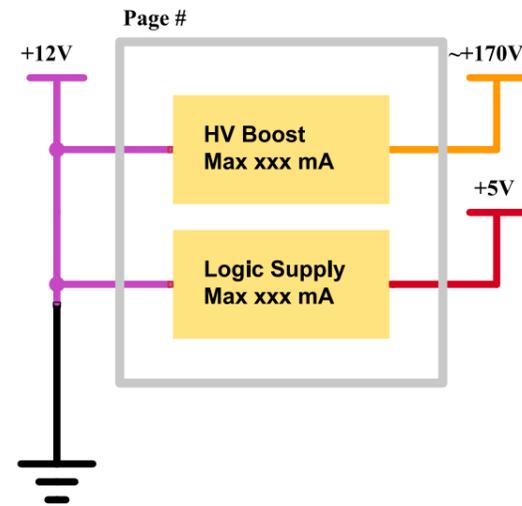
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

Comments:	Company:	Variant:	Git Hash:	
	Company Name	DRAFT	b199c49	
	Board Name:	Project Name:		
Sheet Title:	File Name:	Designer:	Date:	Revision:
/	Nixie_Tube_Clock.kicad_sch	Aidan Brzezinski	2025-01-12	+(Unreleased)
Sheet Path:	Reviewer:	Size:	Sheet:	
/		A3	1 of 7	

[2] Block Diagram

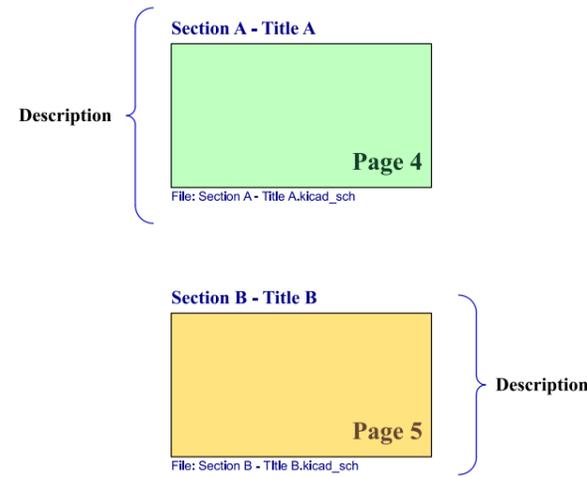


Target specifications:

Input voltage:	?? - ?? V
Spec 2	??
Spec 3	??
Spec 4	??

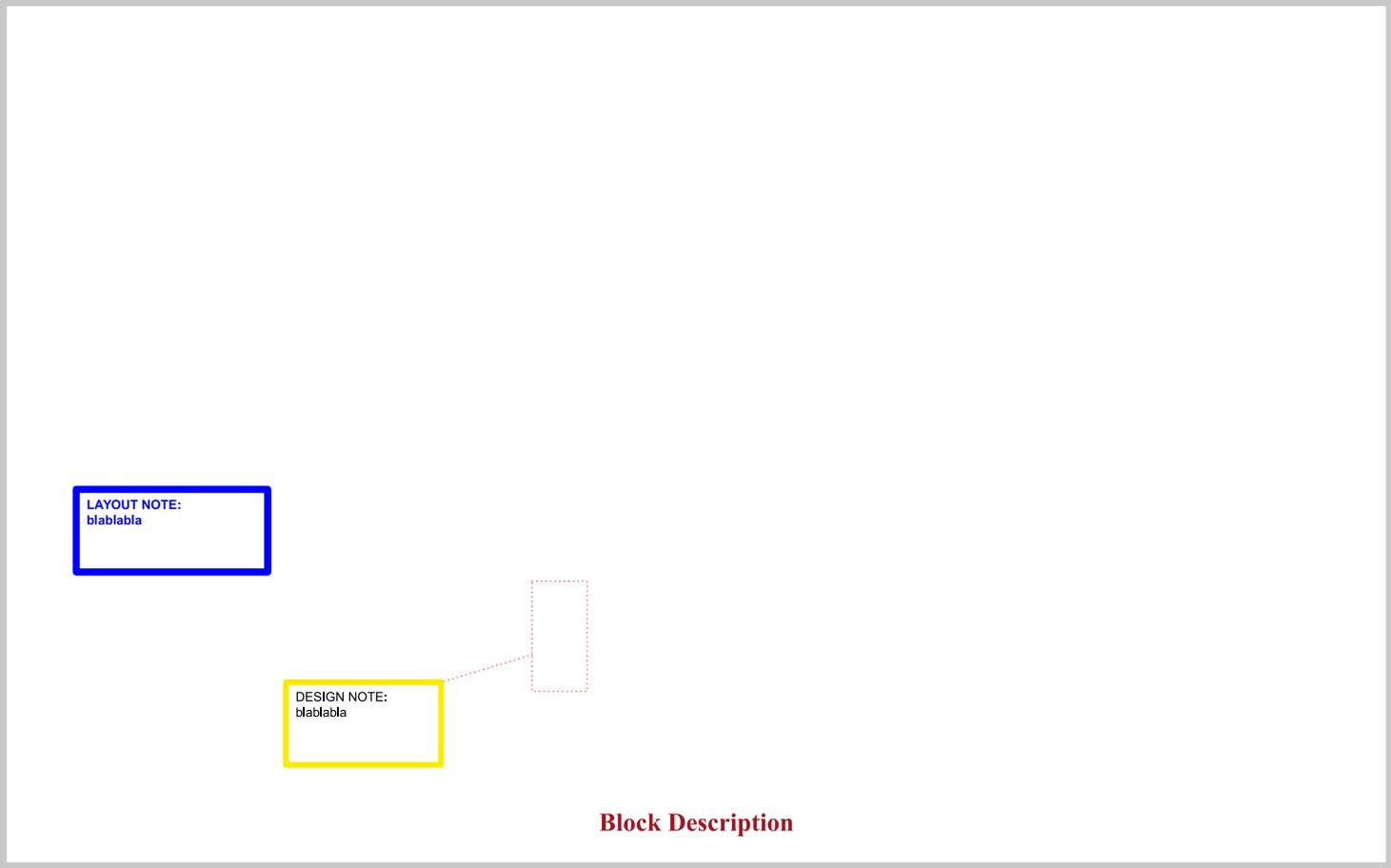
Comments:	Company:	Variant:	Git Hash:
	Company Name	DRAFT	b199c49
Sheet Title:	Board Name:	Project Name:	
	Block Diagram	Nixie Clock Main Board	Nixie Tube Clock
Sheet Path:	File Name:	Designer:	Date:
	/Block Diagram/	Block Diagram.kicad_sch	Aidan Brzezinski
	Reviewer:	Size:	Revision:
		A3	2 of 7

[3] Project Architecture



	Comments:	Company: Company Name	Variant: DRAFT	Git Hash: b199c49
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12
	Sheet Path: /Project Architecture/	Reviewer:	Size: A3	Revision: + (Unreleased)
		Board Name: Nixie Clock Main Board	Project Name: Nixie Tube Clock	
			Date: 2025-01-12	Revision: + (Unreleased)
			Size: A3	Sheet: 3 of 7

[4] Sheet Title A



Block Description

	Comments: AN5346 STM32G474 Datasheet p.81 J. Pieper ADC investigation	Company: Company Name	Variant: DRAFT	Git Hash: b199e49	
		Board Name: Nixie Clock Main Board	Project Name: Nixie Tube Clock		
	Sheet Title: Sheet Title A	File Name: Section A - Title A.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/Section A - Title A/	Reviewer:	Size: A4	Sheet: 4 of 7	

[5] Sheet Title B

Block Description

DESIGN NOTE:
blabla

DESIGN NOTE:
blabla

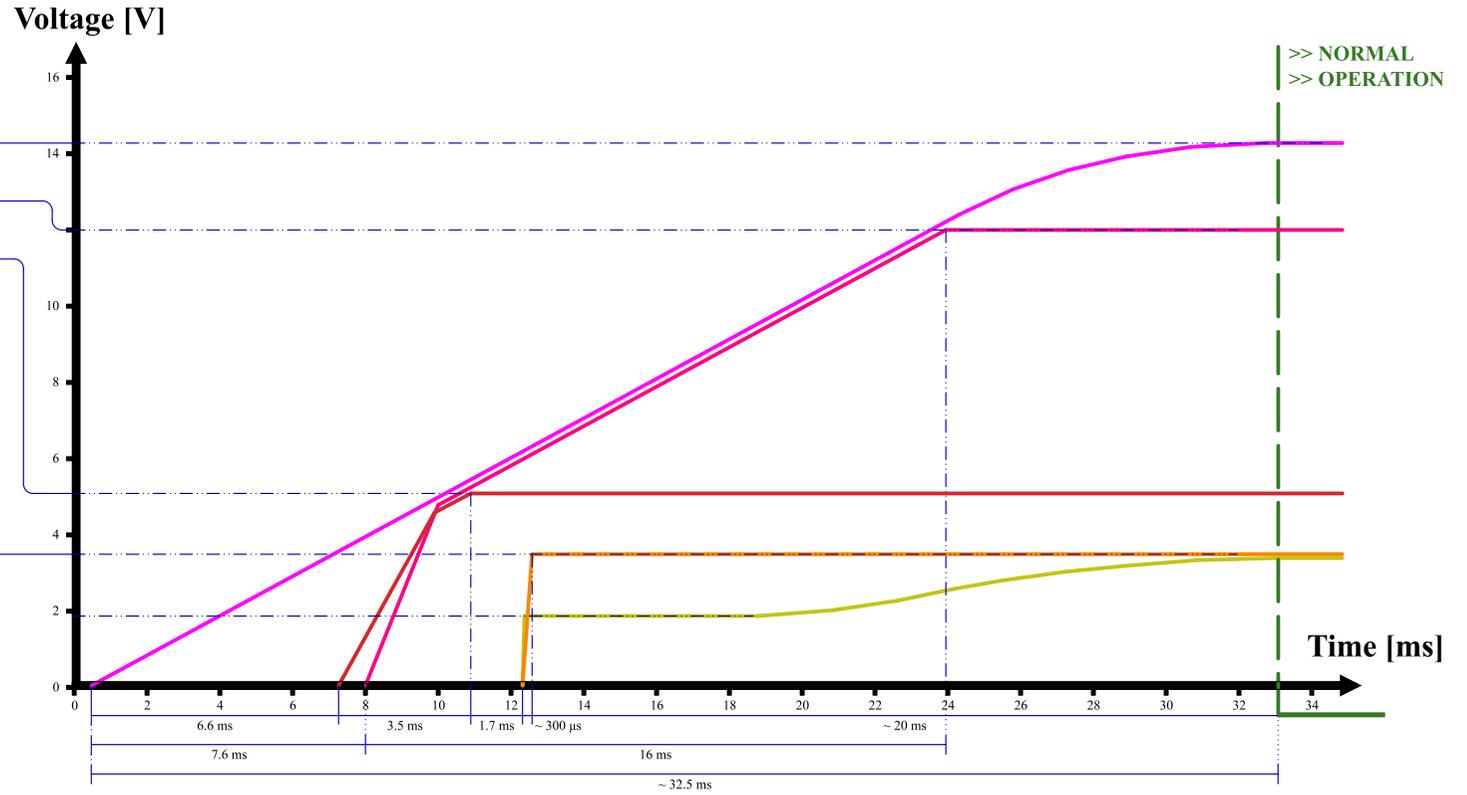
LAYOUT NOTE:
blabla

Block Description

Comments: References: Flexible I/O worked examples Flexible I/O source configuration	Company: Company Name	Variant: DRAFT	Git Hash: b199c49	
	Board Name: Nixie Clock Main Board		Project Name: Nixie Tube Clock	
Sheet Title: Sheet Title B	File Name: Section B - Title B.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
Sheet Path: /Project Architecture/Section B - Title B/		Reviewer:	Size: A3	Sheet: 5 of 7

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company:	Variant:	Git Hash:
		Company Name:	DRAFT	b199e49
	Board Name:	Project Name:		
	Nixie Clock Main Board	Nixie Tube Clock		
Sheet Title:	File Name:	Designer:	Date:	Revision:
Power - Sequencing	Power - Sequencing.kicad_sch	Aidan Brzezinski	2025-01-12	+ (Unreleased)
Sheet Path:	Reviewer:	Size:	Sheet:	
/Power - Sequencing/		A4	6 of 7	

[7] Revision History

	Comments:	Company:		Variant:	Git Hash:
		Company Name		DRAFT	b199e49
		Board Name:		Project Name:	
		Nixie Clock Main Board		Nixie Tube Clock	
Sheet Title:	File Name:	Designer:	Date:	Revision:	
Revision History	Revision History.kicad_sch	Aidan Brzezinski	2025-01-12	+ (Unreleased)	
Sheet Path:		Reviewer:	Size:	Sheet:	
/Revision History/			A4	7 of 7	