

Nixie Clock Main Board

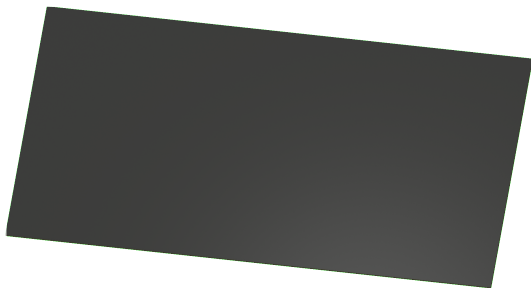
Variant: DRAFT

2026-02-19

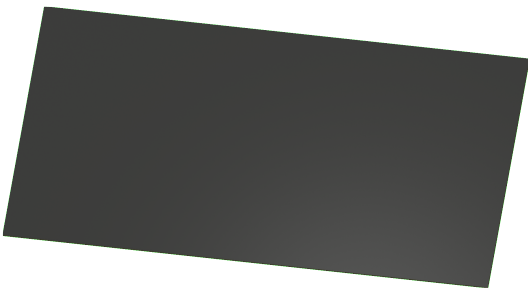
Rev + (Unreleased)

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TOP VIEW



BOTTOM VIEW



NOTES

Comment

Not fitted components are marked as **X**

- DRAFT - Very early stage of schematic, ignore details.
- PRELIMINARY - Close to final schematic.
- CHECKED - There shouldn't be any mistakes. Contact the engineer if you find any.
- RELEASED - A board with this schematic has been sent to production.

Date: 19-Feb-2026

DESIGN CONSIDERATIONS

DESIGN NOTE:
Example text for informational design notes.

DESIGN NOTE:
Example text for debug notes.

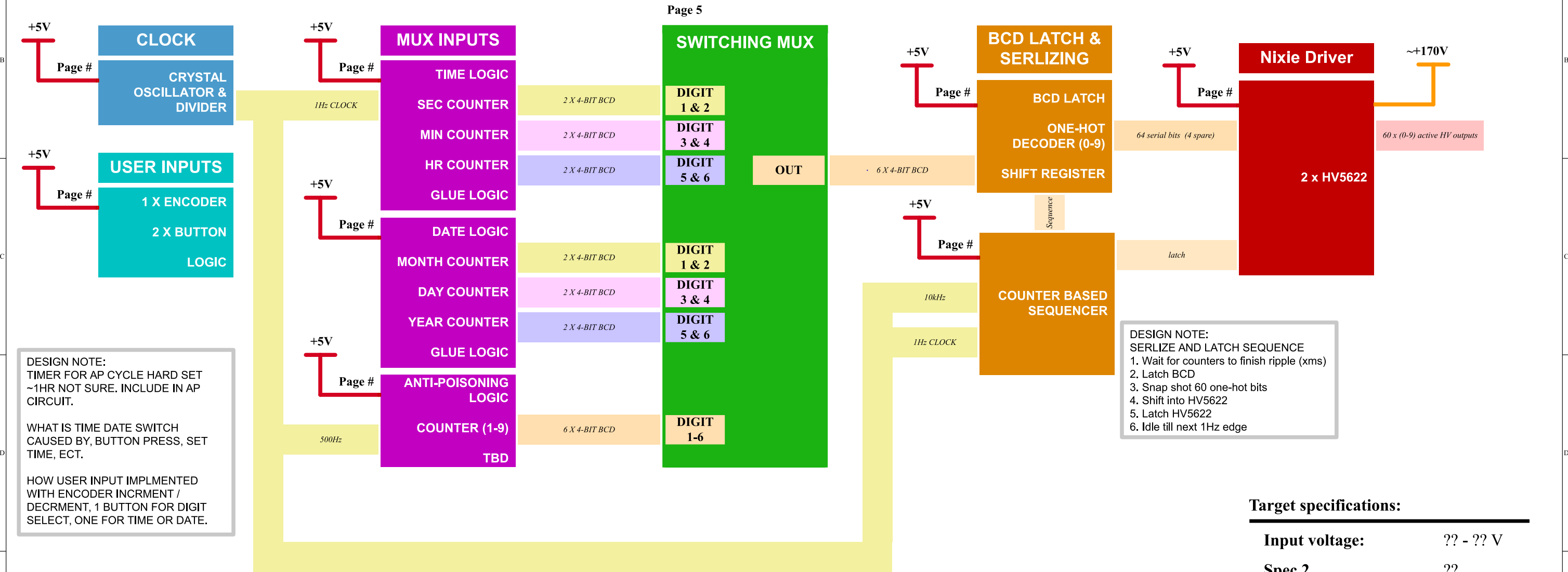
DESIGN NOTE:
Example text for cautionary design notes.

DESIGN NOTE:
Example text for critical design notes.

LAYOUT NOTE:
Example text for critical layout guidelines.

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b199c49
	Sheet Title:	Board Name: Nixie Clock Main Board		Project Name: Nixie Tube Clock	
	Sheet Path: /	File Name: Nixie_Tube_Clock.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
			Reviewer:	Size: A3	Sheet: 1 of 7

[2] Block Diagram



	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b199c49
	Sheet Title: Block Diagram	Board Name: Nixie Clock Main Board		Project Name: Nixie Tube Clock	
	Sheet Path: /Block Diagram/	File Name: Block Diagram.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
	Reviewer:			Size: A3	Sheet: 2 of 7

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b199c49
	Sheet Title: Project Architecture	File Name: Project Architecture.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Project Architecture/		Reviewer:	Size: A3	Sheet: 3 of 7

[5] Sheet Title B

Block Description

DESIGN NOTE:
blabla

DESIGN NOTE:
blabla

LAYOUT NOTE:
blabla

Block Description

Block Description

Block Description

Block Description

Block Description

Block Description

Block Description

Block Description

Comments:

References:
Flexible I/O worked examples
Flexible I/O source configuration

Company:

Company Name

Board Name:
Nixie Clock Main Board

Project Name:
Nixie Tube Clock

Sheet Title:
Sheet Title B

File Name:
Section B - Title B.kicad_sch

Designer:
Aidan Brzezinski

Date:
2025-01-12

Revision:
+ (Unreleased)

Sheet Path:
/Project Architecture/Section B - Title B/

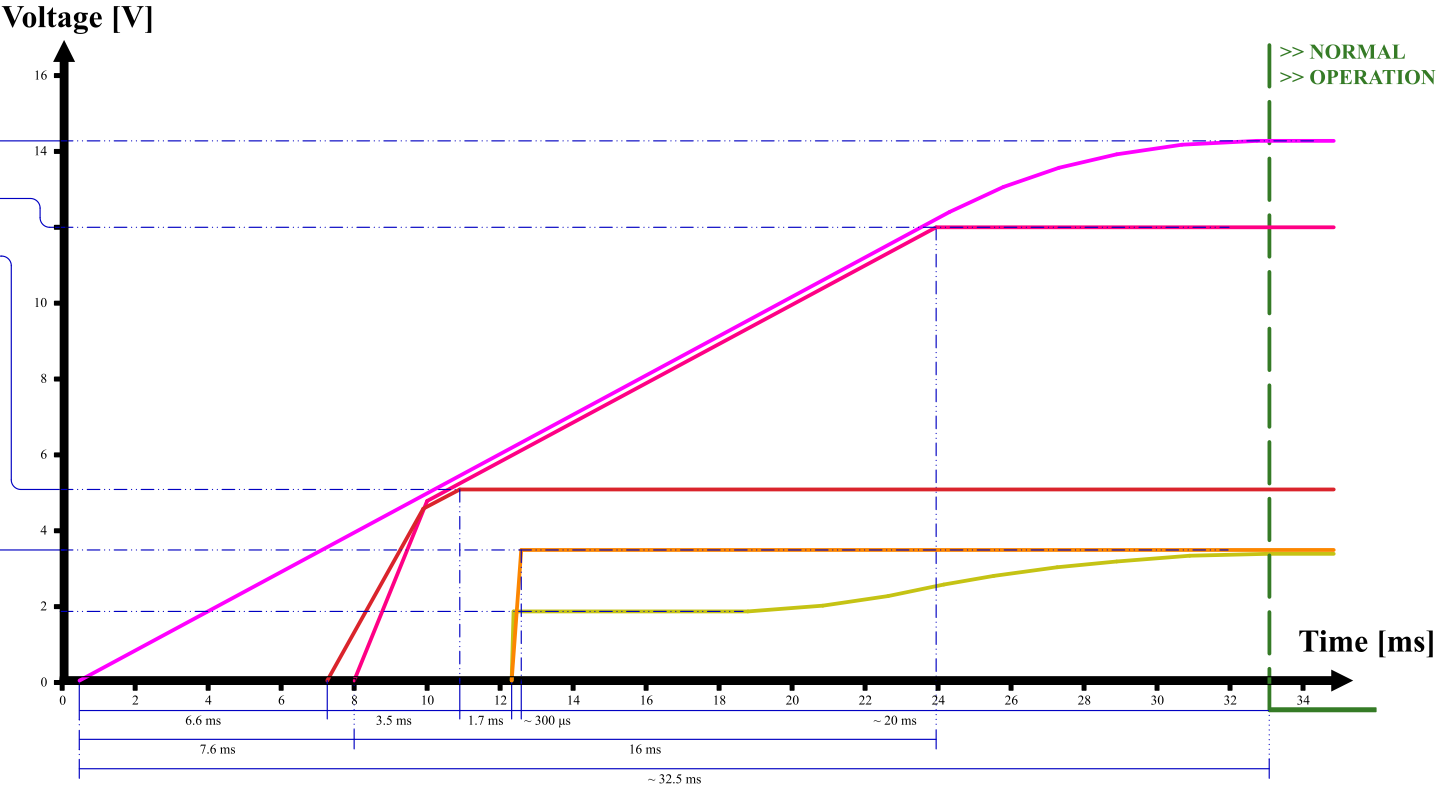
Reviewer:

Size:
A3

Sheet:
5 of **7**

[6] Power - Sequencing

NAME	SOURCE	LEVEL
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%
+V??	??	? - ? ± ??%



	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b199c49
		Board Name: Nixie Clock Main Board		Project Name: Nixie Tube Clock	
	Sheet Title: Power - Sequencing	File Name: Power - Sequencing.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Power - Sequencing/		Reviewer:	Size: A4	Sheet: 6 of 7

[7] Revision History

	Comments:	Company: Company Name		Variant: DRAFT	Git Hash: b199c49
		Board Name: Nixie Clock Main Board		Project Name: Nixie Tube Clock	
	Sheet Title: Revision History	File Name: Revision History.kicad_sch	Designer: Aidan Brzezinski	Date: 2025-01-12	Revision: + (Unreleased)
	Sheet Path: /Revision History/		Reviewer:	Size: A4	Sheet: 7 of 7