

# Nixie Clock Main Board

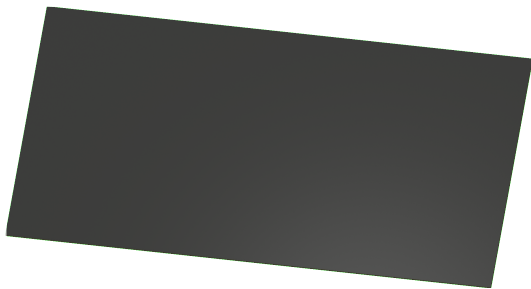
Variant: DRAFT

2026-03-08

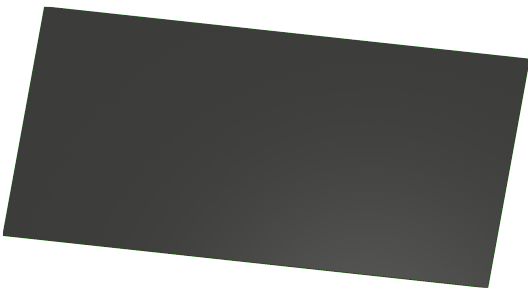
Rev + (Unreleased)

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## TOP VIEW



## BOTTOM VIEW



## NOTES

### Nixie Clock Main Board Document

Comment

Not fitted components are marked as **X**

**DRAFT** - Very early stage of schematic, ignore details.

**PRELIMINARY** - Close to final schematic.

**CHECKED** - There shouldn't be any mistakes. Contact the engineer if you find any.

**RELEASED** - A board with this schematic has been sent to production.

Date: 08-Mar-2026

## DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational design notes.

DESIGN NOTE:  
Example text for debug notes.

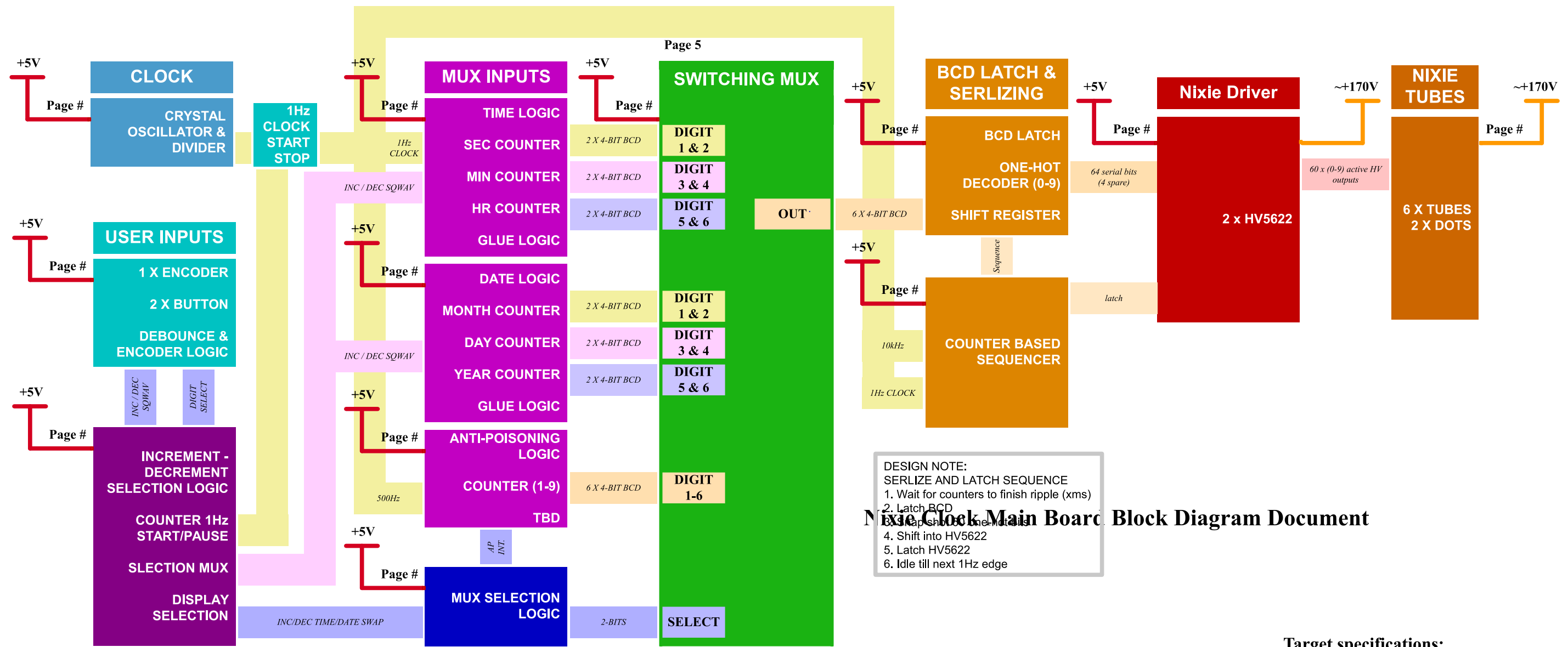
DESIGN NOTE:  
Example text for cautionary design notes.

DESIGN NOTE:  
Example text for critical design notes.

LAYOUT NOTE:  
Example text for critical layout guidelines.

|  |                  |                                          |                               |                                   |                             |
|--|------------------|------------------------------------------|-------------------------------|-----------------------------------|-----------------------------|
|  | Comments:        | Company:<br>Company Name                 |                               | Variant:<br>DRAFT                 |                             |
|  | Sheet Title:     | Board Name:<br>Nixie Clock Main Board    |                               | Project Name:<br>Nixie Tube Clock |                             |
|  | Sheet Path:<br>/ | File Name:<br>Nixie_Tube_Clock.kicad_sch | Designer:<br>Aidan Brzezinski | Date:<br>2025-01-12               | Revision:<br>+ (Unreleased) |
|  |                  |                                          | Reviewer:                     | Size:<br>A3                       | Sheet:<br>1 of 10           |

[2] Block Diagram

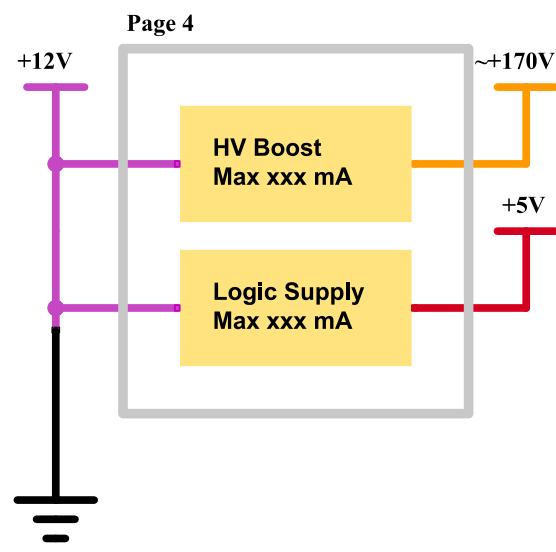


DESIGN NOTE:  
SERLIZE AND LATCH SEQUENCE  
1. Wait for counters to finish ripple (xms)  
2. Latch BCD  
3. Latch shift into HV5622  
4. Shift into HV5622  
5. Latch HV5622  
6. Idle till next 1Hz edge

Nixie Clock Main Board Block Diagram Document

Target specifications:

|                |           |
|----------------|-----------|
| Input voltage: | ?? - ?? V |
| Spec 2         | ??        |
| Spec 3         | ??        |
| Spec 4         | ??        |



DESIGN NOTE:  
TIMER FOR AP CYCLE HARD SET ~1HR NOT SURE. INCLUDE BUTTON SOMEWHERE, NOT DIRECT USER INPUT.

ENCODER FOR INCRMENT DECRMENT, 1 BUTTON FOR CYCLING THROUGH DIGITS, 1 BUTTON FOR SHOWING DATE ~15 SECONDS.

FOR INCREMENT - DECREMENT AN ENCODER INC. AND DEC. AND CYCLE BUTTON. FIRST THE CYCEL BUTTON WILL PAUSE THE 1Hz CLOCK, THEN THE CYCLE BUTTON WILL INCREMENT A COUNTER THAT WILL GO TO A MUX SELECT THAT CHANGES BTWEEN: SS, MM, HH, (AT THIS POINT THE DISPLAY WILL SWAP TOO)MM, DD, YY, THEN ENABLE THE 1Hz CLOCK AGAIN(AND SWAP BACK).

THEN EACH INC, DEC, IS PASSED TROUGH TO THE RESPECTIVE COUNTER AND PUT INTO THE INCREMNT OR DECRMENT INPUT. THE INCRMENT WILL NEED TO BE AND WITH THE RESPECTIVE INCREMNT FOR THAT COUNTER.

IMPORTANT: A COUNTER SHOULD NEVER RECEIVE AN INCREMNT AND DECREMENT AT THE SAME TIME.

|                 |                         |           |                  |                |
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| Comments:       | Company:                |           | Variant:         |                |
|                 | Company Name            |           | DRAFT            |                |
|                 | Board Name:             |           | Project Name:    |                |
| Sheet Title:    | Nixie Clock Main Board  |           | Nixie Tube Clock |                |
|                 | File Name:              |           | Date:            | Revision:      |
|                 | Block Diagram.kicad_sch |           | 2025-01-12       | + (Unreleased) |
| Sheet Path:     |                         | Reviewer: | Size:            | Sheet:         |
| /Block Diagram/ |                         |           | A3               | 2 of 10        |

# [3] Project Architecture

Description {

Clock - Oscillator & Divider

Page 4

File: Clock - Oscillator & Divider.kicad\_sch

MUX Inputs - Time Logic

Page 5

File: Section B - Title B.kicad\_sch

Switching MUX

Page 5

File: Switching MUX.kicad\_sch

MUX Inputs - Date Logic

Page 5

File: MUX Inputs - Date Logic.kicad\_sch

MUX Inputs - Anti-Poisoning Logic

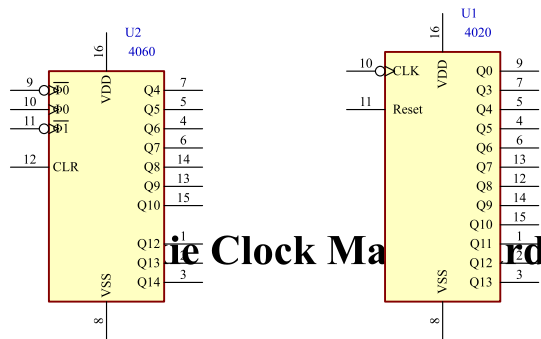
Page 5

File: MUX Inputs - Anti-Poisoning Logic.kicad\_sch

## Nixie Clock Main Board Project Architecture Document

|  |                                       |                                              |                               |                                   |                             |
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|  | Comments:                             | Company:<br>Company Name                     |                               | Variant:<br>DRAFT                 |                             |
|  |                                       | Board Name:<br>Nixie Clock Main Board        |                               | Project Name:<br>Nixie Tube Clock |                             |
|  | Sheet Title:<br>Project Architecture  | File Name:<br>Project Architecture.kicad_sch | Designer:<br>Aidan Brzezinski | Date:<br>2025-01-12               | Revision:<br>+ (Unreleased) |
|  | Sheet Path:<br>/Project Architecture/ |                                              | Reviewer:                     | Size:<br>A3                       | Sheet:<br>3 of 10           |

# [4] Clock



ie Clock Main Board Clock Document

## Block Description

LAYOUT NOTE:  
blablabla

DESIGN NOTE:  
blablabla

### Comments:

AN5346  
STM32G474 Datasheet p.81  
J. Pieper ADC investigation

### Company:

Company Name

### Board Name:

Nixie Clock Main Board

### Variant:

DRAFT

### Project Name:

Nixie Tube Clock

### Sheet Title:

Clock - Oscillator & Divider

### File Name:

Clock - Oscillator & Divider.kicad\_sch

### Designer:

Aidan Brzezinski

### Date:

2025-01-12

### Revision:

+ (Unreleased)

### Sheet Path:

/Project Architecture/Clock - Oscillator & Divider/

### Reviewer:

### Size:

A4

### Sheet:

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| [8] Sheet Title B |                   |   |                   |                   |                   |                   |   |                   |  |  |                   |  |
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Nixie Clock Main Board Sheet Title B Document

|                                                                                                       |                                                               |                                   |                                       |                                 |
|-------------------------------------------------------------------------------------------------------|---------------------------------------------------------------|-----------------------------------|---------------------------------------|---------------------------------|
| Comments:<br><br>References:<br><br>Flexible I/O worked examples<br>Flexible I/O source configuration | Company:<br><br>Company Name                                  |                                   | Variant:<br><br>DRAFT                 |                                 |
|                                                                                                       | Board Name:<br><br>Nixie Clock Main Board                     |                                   | Project Name:<br><br>Nixie Tube Clock |                                 |
|                                                                                                       |                                                               |                                   |                                       |                                 |
| Sheet Title:<br><br>MUX Inputs - Anti-Poisoning Logic                                                 | File Name:<br><br>MUX Inputs - Anti-Poisoning Logic.kicad_sch | Designer:<br><br>Aidan Brzezinski | Date:<br><br>2025-01-12               | Revision:<br><br>+ (Unreleased) |
| Sheet Path:<br><br>/Project Architecture/MUX Inputs - Anti-Poisoning Logic/                           |                                                               | Reviewer:                         | Size:<br><br>A3                       | Sheet:<br><br>8 of 10           |

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| [9] Sheet Title B |                   |                                      |                                   |                                               |            |                |  |
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|                   |                   |                                      | References:                       | Company Name                                  |            | DRAFT          |  |
|                   |                   |                                      | Flexible I/O worked examples      | Board Name:                                   |            | Project Name:  |  |
|                   |                   |                                      | Flexible I/O source configuration |                                               |            |                |  |
|                   |                   | Sheet Title:                         | File Name:                        | Designer:                                     | Date:      | Revision:      |  |
|                   |                   | Switching MUX                        | Switching MUX.kicad_sch           | Aidan Brzezinski                              | 2025-01-12 | + (Unreleased) |  |
|                   |                   | Sheet Path:                          |                                   | Reviewer:                                     | Size:      | Sheet:         |  |
|                   |                   | /Project Architecture/Switching MUX/ |                                   |                                               | A3         | 9 of 10        |  |

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| Block Description  |  | Block Description |  |  | Block Description |  |  |
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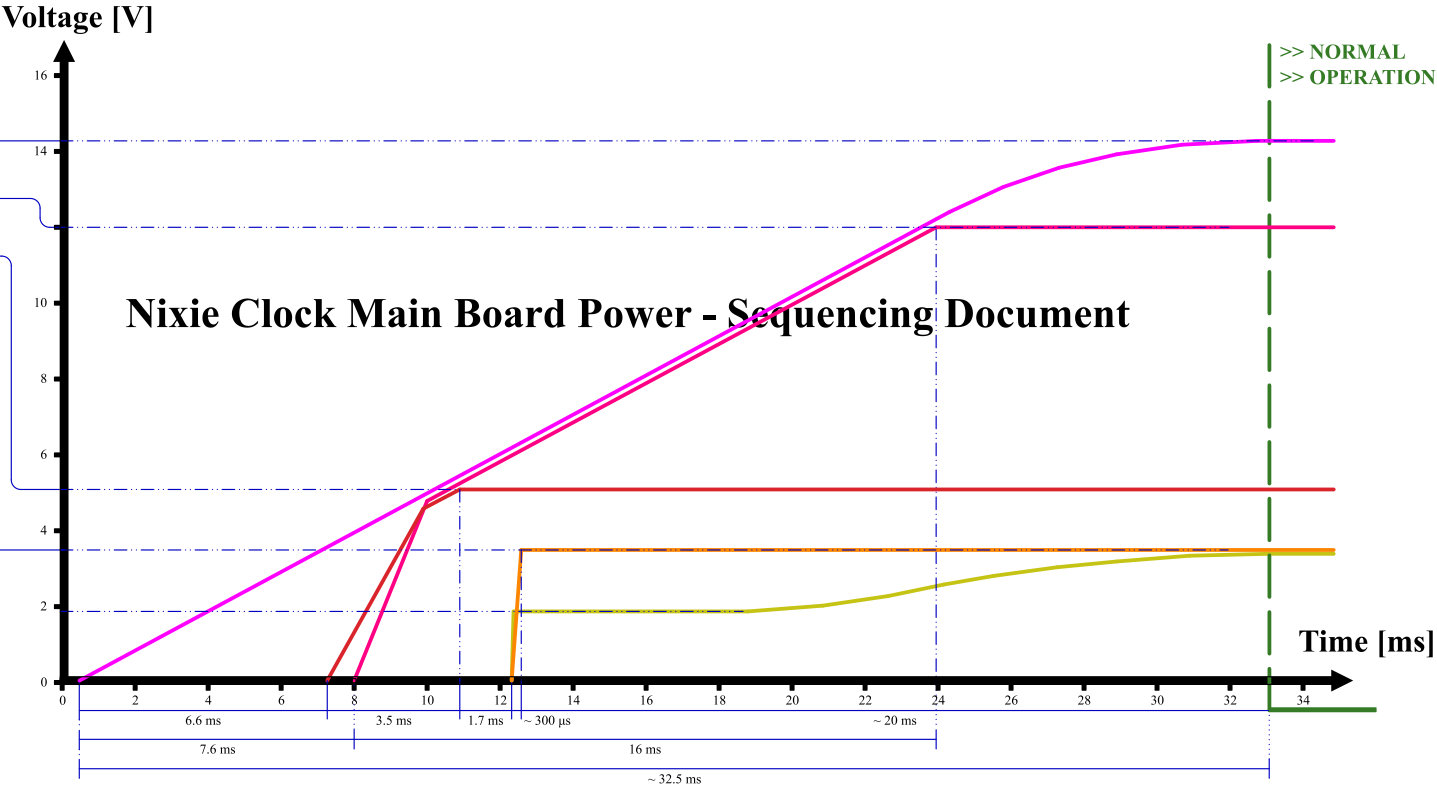
Block Description

Nixie Clock Main Board Sheet Title B Document

|  |                                                                                                                     |                                                            |                                          |                                                     |                                        |
|--|---------------------------------------------------------------------------------------------------------------------|------------------------------------------------------------|------------------------------------------|-----------------------------------------------------|----------------------------------------|
|  | <b>Comments:</b><br><br><b>References:</b><br><br>Flexible I/O worked examples<br>Flexible I/O source configuration | <b>Company:</b><br><br><b>Company Name</b>                 |                                          | <b>Variant:</b><br><br>DRAFT                        |                                        |
|  |                                                                                                                     | <b>Board Name:</b><br><br><b>Nixie Clock Main Board</b>    |                                          | <b>Project Name:</b><br><br><b>Nixie Tube Clock</b> |                                        |
|  | <b>Sheet Title:</b><br><br>MUX Inputs - Date Logic                                                                  | <b>File Name:</b><br><br>MUX Inputs - Date Logic.kicad_sch | <b>Designer:</b><br><br>Aidan Brzezinski | <b>Date:</b><br><br>2025-01-12                      | <b>Revision:</b><br><br>+ (Unreleased) |
|  | <b>Sheet Path:</b><br><br>/Project Architecture/MUX Inputs - Date Logic/                                            |                                                            | <b>Reviewer:</b>                         | <b>Size:</b><br><br><b>A3</b>                       | <b>Sheet:</b><br><br><b>10 of 10</b>   |
|  |                                                                                                                     |                                                            |                                          |                                                     |                                        |

[6] Power - Sequencing

| NAME | SOURCE | LEVEL       |
|------|--------|-------------|
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |
| +V?? | ??     | ? - ? ± ??% |



|  |                      |                              |                  |                  |                |
|--|----------------------|------------------------------|------------------|------------------|----------------|
|  | Comments:            | Company:                     |                  | Variant:         |                |
|  |                      | Company Name                 |                  | DRAFT            |                |
|  |                      | Board Name:                  |                  | Project Name:    |                |
|  |                      | Nixie Clock Main Board       |                  | Nixie Tube Clock |                |
|  | Sheet Title:         | File Name:                   | Designer:        | Date:            | Revision:      |
|  | Power - Sequencing   | Power - Sequencing.kicad_sch | Aidan Brzezinski | 2025-01-12       | + (Unreleased) |
|  | Sheet Path:          |                              | Reviewer:        | Size:            | Sheet:         |
|  | /Power - Sequencing/ |                              |                  | A4               | 6 of 10        |

# [7] Revision History

## Nixie Clock Main Board Revision History Document

|  |                    |                            |                  |                  |                |
|--|--------------------|----------------------------|------------------|------------------|----------------|
|  | Comments:          | Company:                   |                  | Variant:         |                |
|  |                    | Company Name               |                  | DRAFT            |                |
|  |                    | Board Name:                |                  | Project Name:    |                |
|  |                    | Nixie Clock Main Board     |                  | Nixie Tube Clock |                |
|  | Sheet Title:       | File Name:                 | Designer:        | Date:            | Revision:      |
|  | Revision History   | Revision History.kicad_sch | Aidan Brzezinski | 2025-01-12       | + (Unreleased) |
|  | Sheet Path:        |                            | Reviewer:        | Size:            | Sheet:         |
|  | /Revision History/ |                            |                  | A4               | 7 of 10        |